

+---+---+---+---+---+---+---+---+

^

This bit is undefined.

Page 121

Note 1 should read "Bits 6 and 7 are transferred to the Status Register. If the result of A AND M is zero, then Z=1; otherwise Z=0."

Page 127-128

The unimplemented opcodes are shown as NOPs, which is wrong. \$EA is the only code defined as NOP. The others should not be used as they perform undefined operations.

Page 128

Op-code \$AD is a LDA, Absolute

Page 137

The addresses starting at line 100 should be:

```
CLRAN0 EQU $C058
SETAN0 EQU $C059
CLRAN1 EQU $C05A
SETAN1 EQU $C05B
CLRAN2 EQU $C05C
SETAN2 EQU $C05D
CLRAN3 EQU $C05E
SETAN3 EQU $C05F
```

Page 143

Starting at address \$FA6F the comments should read:

```
FA6F LDA CLRAN0 ;AN0 = TTL LO
FA72 LDA CLRAN1 ;AN1 = TTL LO
FA75 LDA SETAN2 ;AN2 = TTL HI
FA78 LDA SETAN3 ;AN3 = TTL HI
```

Page 165

The comment after address \$FCAC should read

1.0204 USEC * (13+27/2*A+5/2*A*A)

Pages 172-176

These tables were cut up to fit the pages so they are no longer in numeric or alphabetic order.

Apple Tech Notes

Tech Info Library Article Number:1062